

Appn No. 10/815,611
Amdt. Dated January 23, 2007
Reply to Office Communication of January 5, 2007

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Amendments to the Specification:

The paragraph beginning at page 84, lines 9-12, is to be amended as follows:

The imaging unit incorporates both the image sensor 2412 and the image processor 2410, which are usefully combined into a single compact chip as described in the co-pending US application ~~USSN~~ application numbers 10/778056 / ~~10/778058, 10/778060, 10/778059, 10/778063, 10/778062, 10/778061, 10/778057~~, entitled "Image Sensor with Digital Framestore"; "Image Sensor with Low-Pass Filter", "Image Sensor with Range Expander"; "Pixel Sensor"; "Image Sensor Timing Circuit"; "Image Processor with Low Power Mode"; "Image Processor"; "Synchronization Protocol", respectively, (docket no. NPS047-US-NPS054) and filed 17 February 2004.